

FIG. 1

LOAD/MOVE DUPLICATE INSTRUCTIONS FOR A  
PROCESSOR

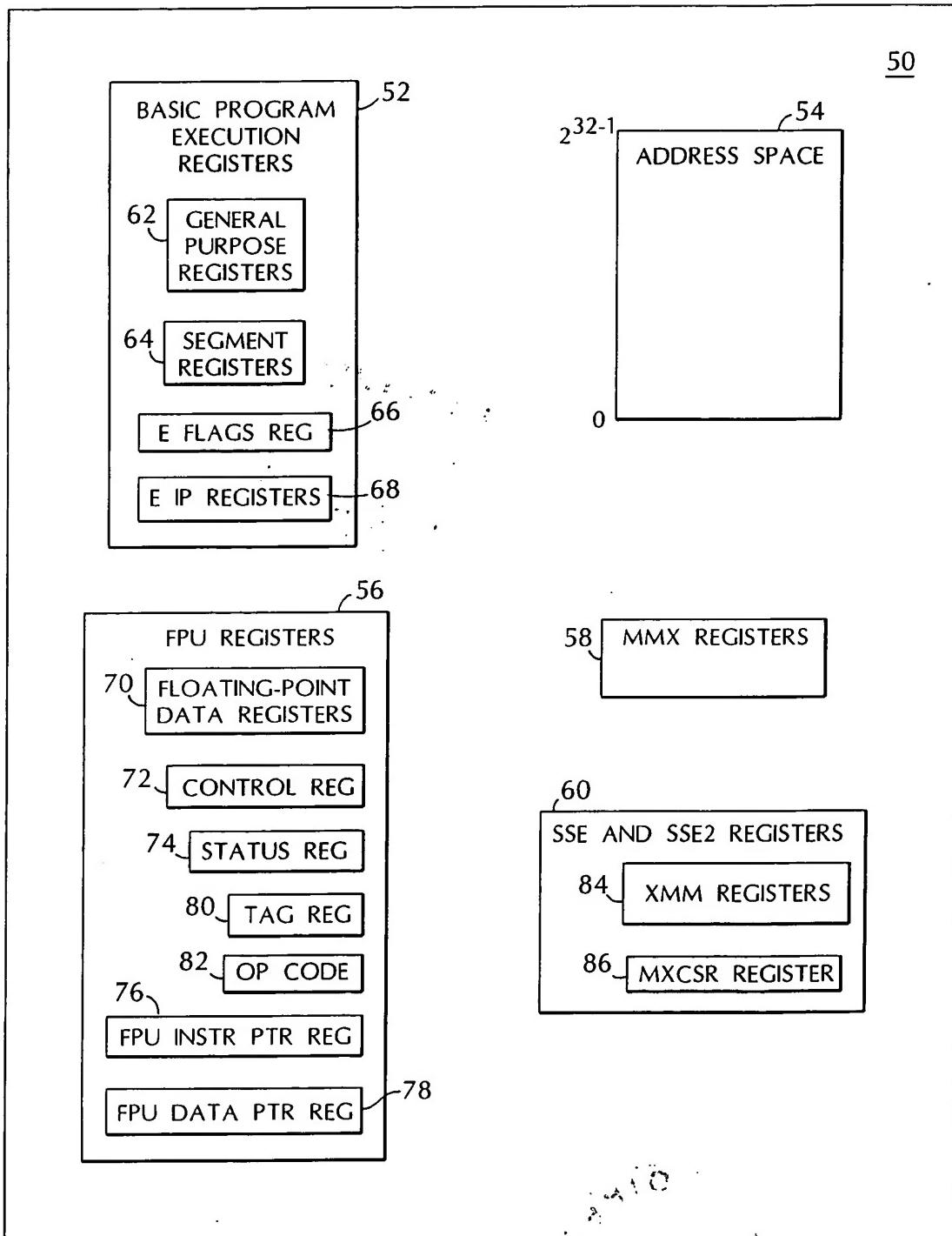


FIG. 2

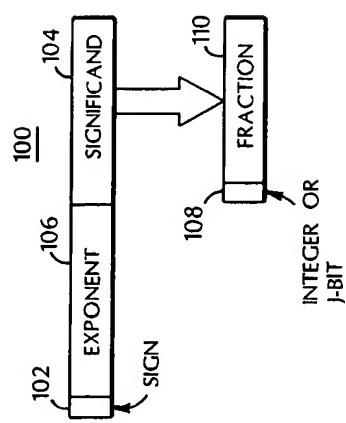
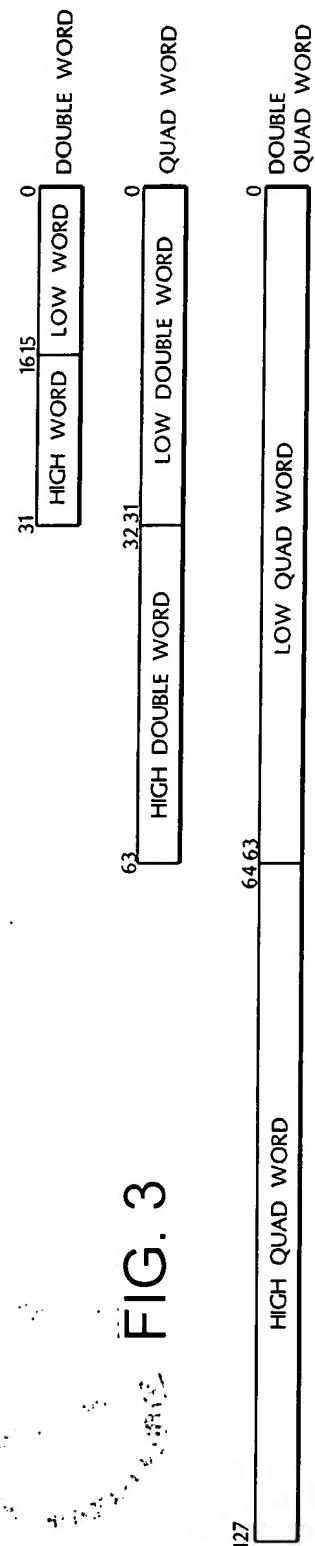


FIG. 3



120

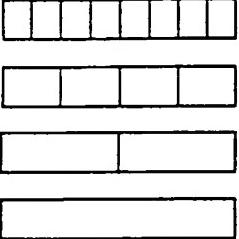
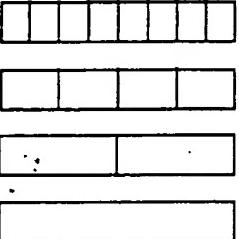
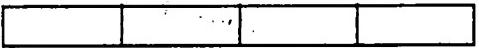
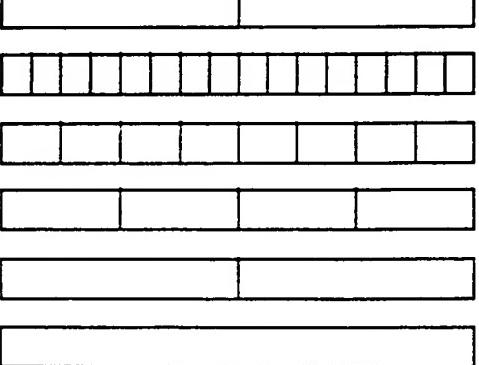
SIMD Extension	Register Layout	Data Type
		MMX Registers — 58
MMX Technology		8 Packed Byte Integers 4 Packed Word Integers 2 Packed Doubleword Integers Quadword
		MMX Registers — 58
SSE		8 Packed Byte Integers 4 Packed Word Integers 2 Packed Doubleword Integers Quadword
		XMM Registers — 84
		4 Packed Single-Precision Floating-Point Values
		MMX Registers — 58
SSE2		2 Packed Doubleword Integers Quadword
		XMM Registers — 84
		2 Packed Double-Precision Floating-Point Values 16 Packed Byte Integers 8 Packed Word Integers 4 Packed Doubleword Integers 2 Quadword Integers Double Quadword

FIG. 5